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┌─────────────────────────────────┐
│       SESSION 1 – TAPA II        │
│        Plenary Session           │
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Thursday, June 17, 8:00 a.m.
Chairpersons: Bruce Gieseke, AMD
                  Tadahiro Kuroda, Keio University

8:00 a.m.
Welcome and Opening Remarks
Shekahr Borkar, Intel
Tadashi Nishimura, Renesas Technology

1.1 — 8:35 a.m
Image Processing Chain in Digital Still Cameras,
Albert Theuwissen, Dalsa

1.2 — 9:20 a.m
Semiconductor Technologies Support New Generation Hybrid Car,
Touma Fujikawa, Toyota Motor